

TOSHIBA CMOS Linear Integrated Circuit Silicon Monolithic

TCR5RG series

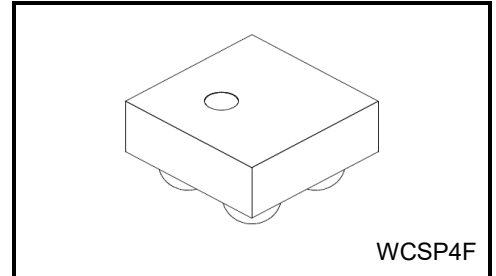
Ultra high Ripple rejection ratio, 500 mA CMOS Low Dropout Regulator in ultra small package

1. Description

The TCR5RG series are CMOS general-purpose single-output voltage regulators with an on/off control input, featuring low dropout voltage and ultra high Ripple rejection ratio.

These voltage regulators are available in fixed output voltages between 0.9 V and 5.0 V and capable of driving up to 500 mA. They feature Overcurrent protection, Thermal shutdown and Auto-discharge.

The TCR5RG series is offered in the ultra small plastic mold package WCSP4F (0.645 mm x 0.645 mm (typ.); t 0.33 mm (max)) and has a high ripple rejection ratio of 100 dB (f = 1 kHz, 2.8 V output). As small ceramic input and output capacitors 1 μ F can be used with the TCR5RG series, these devices are ideal for portable applications that require high-density board assembly such as cellular phones.



2. Applications

Power IC developed for portable applications

3. Features

- Ultra small package WCSP4F (0.645 mm x 0.645 mm (typ.); t 0.33 mm (max)).
- High Ripple rejection ratio 100 dB (Typ.) @1 kHz at 2.8 V-output
- High Ripple rejection ratio 93 dB (Typ.) @10 kHz at 2.8 V-output
- High Ripple rejection ratio 67 dB (Typ.) @100 kHz at 2.8 V-output
- High Ripple rejection ratio 59 dB (Typ.) @1 MHz at 2.8 V-output
- Low output noise voltage ($V_{NO} = 5 \mu V_{rms}$ (Typ.) at $10\text{Hz} \leq f \leq 100\text{kHz}$)
- Low quiescent current ($I_B = 7 \mu A$ (Typ.) at $I_{OUT} = 0 \text{ mA}$)
- Overcurrent protection
- Thermal shutdown
- Auto-discharge
- Low Dropout voltage
 $V_{DO} = 150 \text{ mV}$ (Typ.) at 2.8 V-output, $I_{OUT} = 500 \text{ mA}$
- Wide range output voltage line up ($V_{OUT} = 0.9 \text{ to } 5.0 \text{ V}$)
- Pull down connection between CONTROL and GND
- Ceramic capacitors can be used ($C_{IN} = 1 \mu F$, $C_{OUT} = 1 \mu F$)

Start of commercial production
2020-12

4. Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Input voltage	V _{IN}	-0.3 to 6.0	V
Control voltage	V _{CT}	-0.3 to V _{IN} + 0.3 ≤ 6.0	V
Output voltage	V _{OUT}	-0.3 to V _{IN} + 0.3 ≤ 6.0	V
Output current	I _{OUT}	500	mA
Power dissipation	P _D	800 (Note1)	mW
Junction temperature	T _j	150	°C
Storage temperature range	T _{stg}	-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

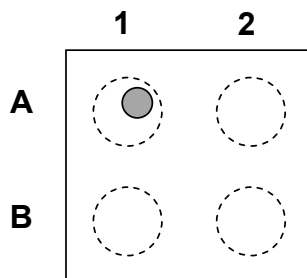
Note1: Rating at mounting on a board

Glass epoxy(FR4) board dimension: 40mm x 40mm x 1.6mm, both sides of board.

Metal pattern ratio: a surface approximately 50%, the reverse side approximately 50%

Through hole hall: diameter 0.5 mm x 24

5. Pin Assignment (top view)



	1	2
A	V _{IN}	V _{OUT}
B	CONTROL	GND

6. Operating Ranges

Characteristics	Symbol	Rating	Unit
Input voltage	V _{IN}	1.8 to 5.5 (Note 2)	V
Control voltage	V _{CT}	0 to V _{IN}	V
Output voltage	V _{OUT}	0.9 to 5.0	V
Output current	I _{OUT}	DC 500	mA
Operation Temperature	T _{opr}	-40 to 85	°C
Output Capacitance	C _{OUT}	≥ 1.0 μF	—
Input Capacitance	C _{IN}	≥ 1.0 μF	—

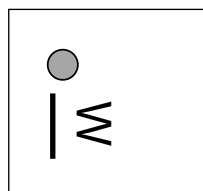
Note 2: Please refer to Dropout Voltage table (Page 6) and use it within Absolute Maximum Ratings Junction temperature and Operation Temperature Ranges.

7. List of Products Number, Output voltage and Marking

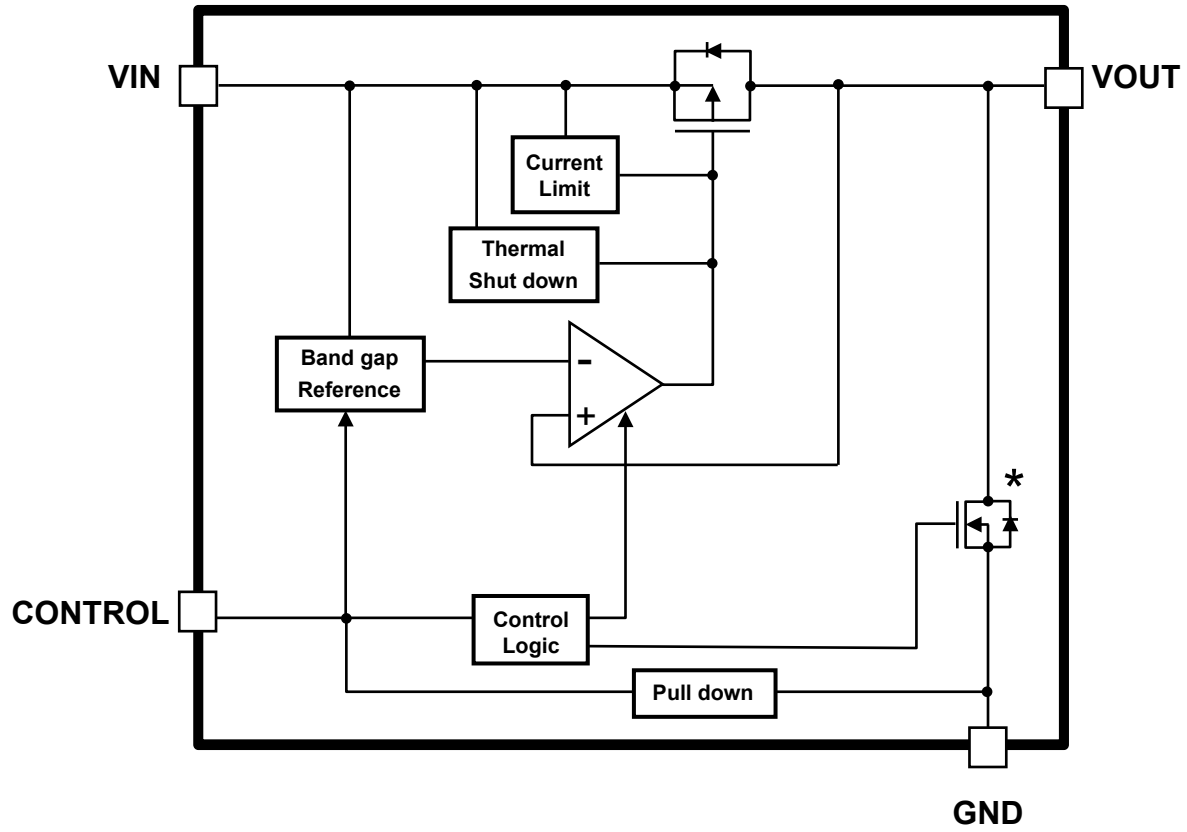
Product No.	Output voltage(V)	Marking
TCR5RG09A	0.9	<u>A</u>
TCR5RG095A	0.95	<u>B</u>
TCR5RG10A	1.0	<u>C</u>
TCR5RG105A	1.05	<u>D</u>
TCR5RG11A	1.1	<u>E</u>
TCR5RG115A	1.15	<u>F</u>
TCR5RG12A	1.2	<u>N</u>
TCR5RG1225A	1.225	<u>A</u>
TCR5RG125A	1.25	<u>B</u>
TCR5RG13A	1.3	<u>H</u>
TCR5RG135A	1.35	<u>J</u>
TCR5RG14A	1.4	<u>C</u>
TCR5RG15A	1.5	<u>K</u>
TCR5RG16A	1.6	<u>D</u>
TCR5RG17A	1.7	<u>L</u>
TCR5RG175A	1.75	<u>M</u>
TCR5RG18A	1.8	<u>N</u>
TCR5RG1825A	1.825	<u>P</u>
TCR5RG185A	1.85	<u>P</u>
TCR5RG19A	1.9	<u>R</u>
TCR5RG20A	2.0	<u>S</u>
TCR5RG25A	2.5	<u>T</u>
TCR5RG255A	2.55	<u>L</u>
TCR5RG27A	2.7	<u>V</u>
TCR5RG28A	2.8	<u>W</u>
TCR5RG285A	2.85	<u>X</u>
TCR5RG29A	2.9	<u>Y</u>
TCR5RG2925A	2.925	<u>M</u>
TCR5RG295A	2.95	<u>R</u>
TCR5RG30A	3.0	<u>0</u>
TCR5RG31A	3.1	<u>1</u>
TCR5RG32A	3.2	<u>2</u>
TCR5RG33A	3.3	<u>3</u>
TCR5RG34A	3.4	<u>E</u>
TCR5RG35A	3.5	<u>4</u>
TCR5RG36A	3.6	<u>5</u>
TCR5RG40A	4.0	<u>F</u>
TCR5RG41A	4.1	<u>S</u>
TCR5RG42A	4.2	<u>6</u>
TCR5RG43A	4.3	<u>H</u>
TCR5RG45A	4.5	<u>7</u>
TCR5RG46A	4.6	<u>8</u>
TCR5RG47A	4.7	<u>J</u>
TCR5RG48A	4.8	<u>K</u>
TCR5RG50A	5.0	<u>9</u>

Top Marking (top view)

Example: TCR5RG28A (2.8 V output)



Block Diagram



*Active discharge: Version A only

8. Electrical Characteristics

(Unless otherwise specified, $V_{IN} = V_{OUT} + 0.5V$ ($V_{OUT} > 1.5V$), $V_{IN} = 2.0V$ ($V_{OUT} \leq 1.5V$), $C_{IN} = C_{OUT} = 1\mu F$)

Characteristics	Symbol	Test Condition	$T_J = 25^\circ C$			$T_J = -40 \text{ to } 85^\circ C$ (Note 6)		Unit	
			Min	Typ.	Max	Min	Max		
Output voltage accuracy	V_{OUT}	$I_{OUT} = 1 \text{ to } 500 \text{ mA}$ (Note 3)	$V_{OUT} < 1.8V$	—	—	—	-36	+36	mV
			$1.8V \leq V_{OUT} \leq 2.8V$	—	—	—	-1.5	+1.5	%
			$2.8V < V_{OUT}$				-1.8	+1.8	
Line regulation	Reg·line	$I_{OUT} = 1 \text{ mA}$ (Note 3)	—	0.5	—	—	—	mV	
Load regulation	Reg·load	$1 \text{ mA} \leq I_{OUT} \leq 500 \text{ mA}$	—	3	—	—	—	mV	
Quiescent current	$I_{B(ON)}$	$I_{OUT} = 0 \text{ mA}$ (Note 5)	—	7	—	—	13	μA	
Stand-by current	$I_{B(OFF)}$	$V_{CT} = 0V$	—	0.08	—	—	1.0	μA	
Control pull down current	I_{CT}	—	—	0.08	—	—	1.0	μA	
Drop-out voltage (Note 7)	V_{DO}	$I_{OUT} = 500 \text{ mA}$	$V_{OUT} = 1.8V$	—	235	—	—	290	mV
			$V_{OUT} = 2.8V$	—	150	—	—	210	mV
Output noise voltage	V_{NO}	$I_{OUT} = 10 \text{ mA}$ $10 \text{ Hz} \leq f \leq 100 \text{ kHz}$ (Note 4)	—	5	—	—	15	μV_{rms}	
		$I_{OUT} = 250 \text{ mA}$ $10 \text{ Hz} \leq f \leq 100 \text{ kHz}$ (Note 4)	—	5	—	—	15		
Ripple rejection ratio	R.R.	$I_{OUT} = 10 \text{ mA}$, $V_{IN} = V_{OUT} + 1V$ $V_{Ripple} = 200 \text{ mV}_{p-p}$, (Note 4)	$f = 1 \text{ kHz}$	—	100	—	—	—	dB
			$f = 10 \text{ kHz}$	—	93	—	—	—	dB
			$f = 100 \text{ kHz}$	—	67	—	—	—	dB
			$f = 1 \text{ MHz}$	—	59	—	—	—	dB
Load transient response	ΔV_{OUT}	$I_{OUT} = 1 \text{ mA} \rightarrow 500 \text{ mA}$	—	-40	—	—	—	mV	
		$I_{OUT} = 500 \text{ mA} \rightarrow 1 \text{ mA}$	—	+40	—	—	—	mV	
Output voltage slew rate	V_{OUTSR}	—	—	30	—	—	—	$\text{mV}/\mu s$	
Startup time	t _{ss}	from CONTROL rising edge to 95% V_{OUT} (Note 4)	—	160	—	—	—	μs	
Output current limit	I_{CL}	—	—	700	—	600	850	mA	
Thermal shutdown threshold	T_{SDH}	T_J rising	—	160	—	—	—	$^\circ C$	
	T_{SDL}	T_J falling	—	140	—	—	—	$^\circ C$	
Control voltage (ON)	$V_{CT(ON)}$	—	—	—	—	0.9	5.0	V	
Control voltage (OFF)	$V_{CT(OFF)}$	—	—	—	—	0	0.5	V	
Discharge on resistance	R_{SD}	(Note4)	—	30	—	—	—	Ω	

Note 3: stable state with fixed I_{OUT} condition

$((V_{OUT} + 0.5V) \text{ or } 2V \text{ whichever is greater}) \leq V_{IN} \leq 5.5V$

Note 4: $V_{OUT} = 2.8V$

Note 5: except Control pull down current (I_{CT})

Note 6: This parameter is warranted by design.

Note 7: $V_{DO} = V_{IN1} - (V_{OUT1} \times 0.97)$

V_{OUT1} is the nominal output voltage.

V_{IN1} is the input voltage at which the output voltage becomes 97% of V_{OUT1} after gradually decreasing the input voltage.

Dropout voltage table

($C_{IN} = 1.0 \mu F$, $C_{OUT} = 1.0 \mu F$)

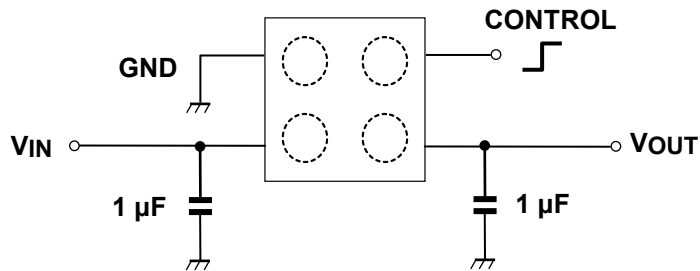
Output voltages	$I_{OUT} = 500 \text{ mA}$			Unit
	Min	Typ.	Max (Note 8)	
$0.9 \text{ V} \leq V_{OUT} \leq 1.35 \text{ V}$	—	(Note 9)	(Note 9)	mV
1.4 V	—	320 (Note 9)	460 (Note 9)	mV
1.5 V	—	310	400	mV
1.6 V	—	260	350	mV
1.7 V	—	250	320	mV
1.75 V	—	240	300	mV
1.8 V	—	235	290	mV
1.825 V	—	235	285	mV
1.85 V	—	230	280	mV
1.9 V	—	225	270	mV
2.0 V	—	215	255	mV
2.5 V, 2.55 V	—	170	230	mV
2.7 V	—	160	215	mV
2.8 V, 2.85 V	—	150	210	mV
2.9 V, 2.925 V, 2.95 V	—	150	205	mV
3.0 V, 3.1 V	—	145	205	mV
3.2 V	—	140	195	mV
3.3 V, 3.4 V	—	135	190	mV
3.5 V, 3.6 V	—	130	185	mV
4.0 V, 4.1 V	—	115	175	mV
4.2 V, 4.3 V	—	115	170	mV
4.5 V	—	115	165	mV
4.6 V, 4.7 V, 4.8 V	—	110	165	mV
5.0 V	—	108	160	mV

Note 8: $T_j = -40$ to $85 \text{ }^\circ\text{C}$. This parameter is guaranteed by design

Note 9: Operating Voltage of V_{IN} should be over 1.8 V.

9. Application Note

9.1. Recommended Application Circuit



The figure above shows the recommended configuration for using a Low-Dropout regulator. Insert a capacitor at V_{OUT} and V_{IN} pins for stable input/output operation. (Ceramic capacitors can be used).

9.2. Power Dissipation

Board-mounted power dissipation ratings are available in the Absolute Maximum Ratings table. Power dissipation is measured on the board condition shown below.

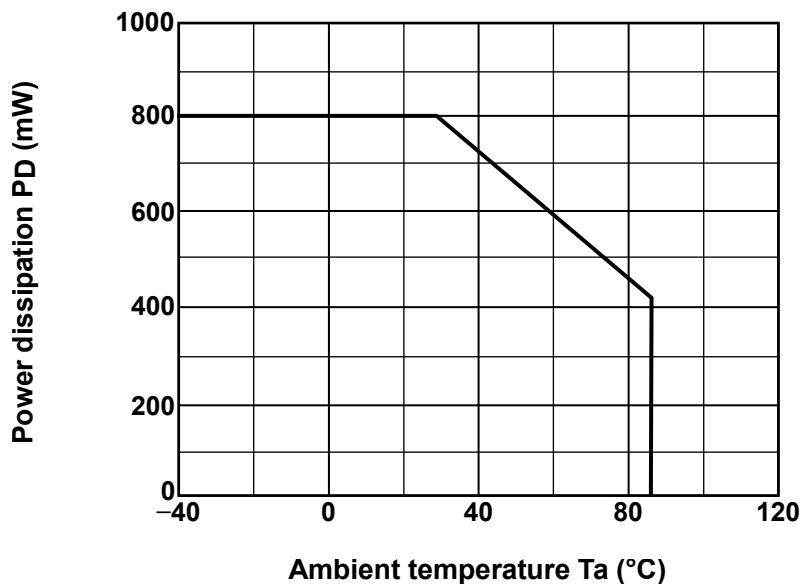
[The Board Condition]

Board material: Glass epoxy (FR4)

Board dimension: 40 mm x 40 mm (both sides of board), t = 1.6 mm

Metal pattern ratio: a surface approximately 50 %, the reverse side approximately 50 %

Through hole: diameter 0.5 mm x 24



9.3. Attention in Use

- Output Capacitors

Toshiba recommends to use ceramic capacitors for these devices. However, because of the type of the capacitors, there might be greatly affected by thermal features and DC bias depending. Please secure an effective capacitance of 0.6 μ F or more by considering application condition for selecting capacitors.

- Mounting

The long distance between IC and output capacitor might affect phase assurance by impedance in wire and inductor. For stable power supply, output capacitor need to mount near IC as much as possible. Also VIN and GND pattern need to be large and make the wire impedance small as possible.

- Permissible Loss

Please have enough design patterns for expected maximum permissible loss. And under consideration of surrounding temperature, input voltage, and output current etc., we recommend proper dissipation ratings for maximum permissible loss; in general maximum dissipation rating is 70 to 80 %.

- Over current Protection and Thermal shut down function

Over current protection and Thermal shut down function are designed in these products, but these are not designed to constantly ensure the suppression of the device within operation limits. Depending on the condition during actual usage, it could affect the electrical characteristic specification and reliability. Also note that if output pins and GND pins are not completely shorted out, these products might be break down. When using these products, please read through and understand the concept of dissipation for absolute maximum ratings from the above mention or our 'Semiconductor Reliability Handbook'. Then use these products under absolute maximum ratings in any condition. Furthermore, Toshiba recommend inserting failsafe system into the design.

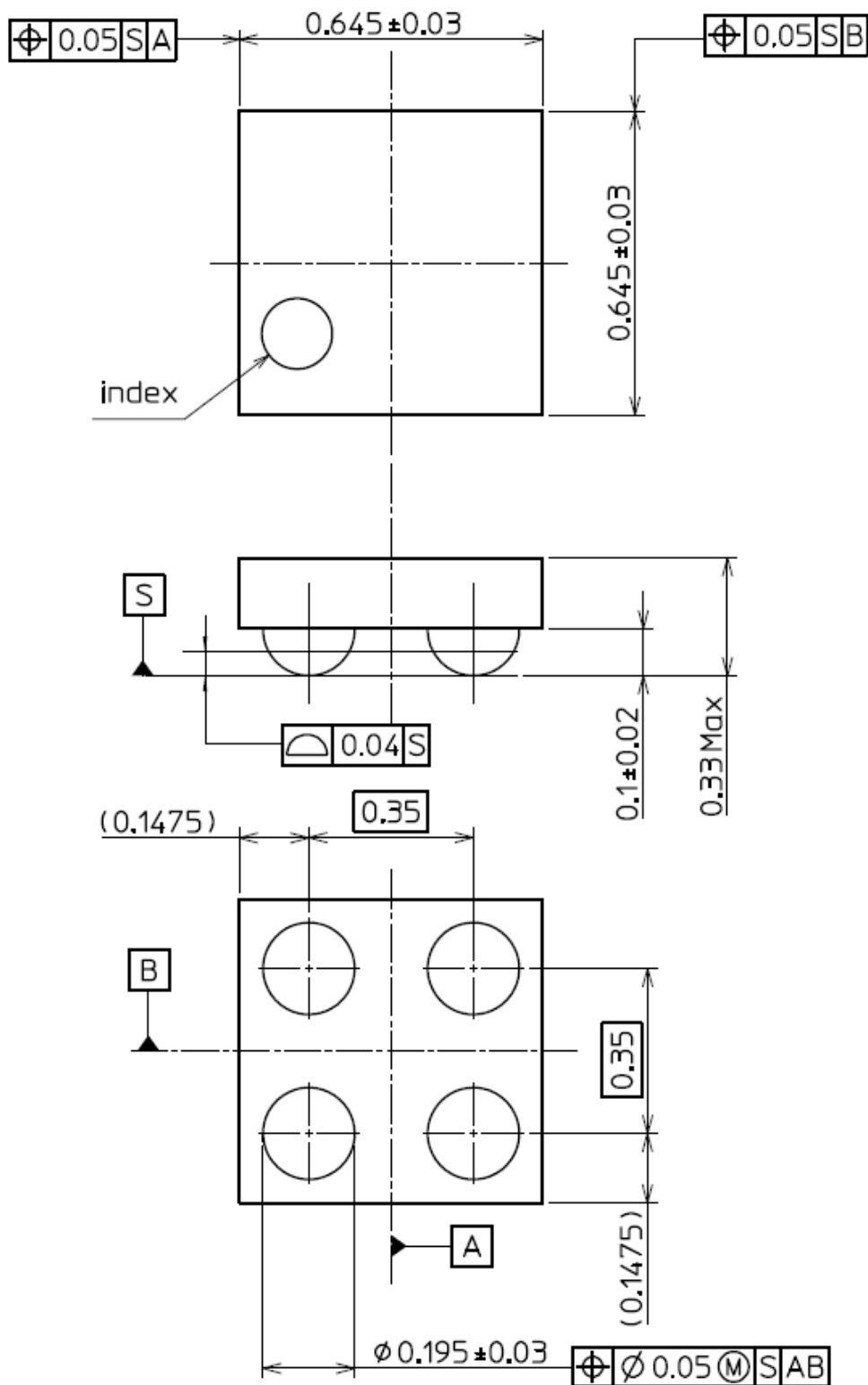
- High ripple rejection ratio and low output noise voltage characteristics

TCR5RG series has low-pass filter which contributes high ripple rejection ratio and low output noise voltage. This low-pass filter turns on about 2ms after V_{OUT} becomes near the nominal V_{OUT}. Therefore, before and when the low-pass filter is turned on, please be careful about the increase and decrease of V_{OUT} such as CONTROL voltage from low to high and road transient response.

10. Package Information

WCSP4F

Unit: mm



Weight: 0.26 mg (typ.)

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